

SHEET 1 OF 1

**INFORMATION DISCLOSURE
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ATTY. DOCKET NO.
070456-0105SERIAL NO.
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Tatsuo HIRAMATSU, et al.FILING DATE
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2183**U.S. PATENT DOCUMENTS**

EXAMINER'S INITIALS	CITE NO.	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		US 2001/0010074 A1	07-26-2001	NISHIHARA et al.	
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EXAMINER'S INITIALS	CITE NO.	Foreign Patent Document Country Codes -Number 4 - Kind Codes (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Figures Appear	Translation Yes No
		GB 2 333 625 A	07-28-1999	LUCENT TECHNOLOGIES INC.		English (Orig.)

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
/G.G./		European Search Report issued in European Patent Application No. EP 04747279.0-2211/1669868 PCT/JP2004009811, dated February 23, 2009.
/G.G./		Koichiro FURUTA et al., "Spatial-Temporal Mapping of Real Application on a Dynamically Reconfigurable Logic Engine (DRLE) LSI, IEEE 2000 Custom Integrated Circuits Conference, pp.151-154.
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EXAMINER

/George Giroux/

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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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